

Integrating Tests of Autonomy with SW and People: Autonomy <u>Requirements Tester (ART)</u> <u>AIAA-HSI ATS 2017</u> Carroll Thronesbery, Ayman Qaddumi, Michael Merta, Eugene McMahon, Mike Monahan

Background: core Flight System (cFS)

- Autonomy Requirements Tester (ART) is design to be compatible with core Flight System (cFS)
- cFS architecture and software
 - Software platform developed by Goddard Space Flight Center (GSFC)
 - Reusable software framework across multiple projects
 - Set of reusable software applications
 - Dynamic run-time environment (real-time constraints)
 - Layered software
 - Component based design
 - <u>Publish-subscribe message communication</u> to make component apps independent
- ART can easily be modified to fit any pub-sub architecture





Autonomy Requirements Tester (ART): Human Centered View

- A tool to support software developers
 - Especially flight software
- People Tasks:
 - Capture autonomy requirements
 - Generate test specifications
 - Execute the test specs
 - Report results
 - Iterate for test-driven development





cFS-based Autonomy Requirements Tester (ART) project: System View

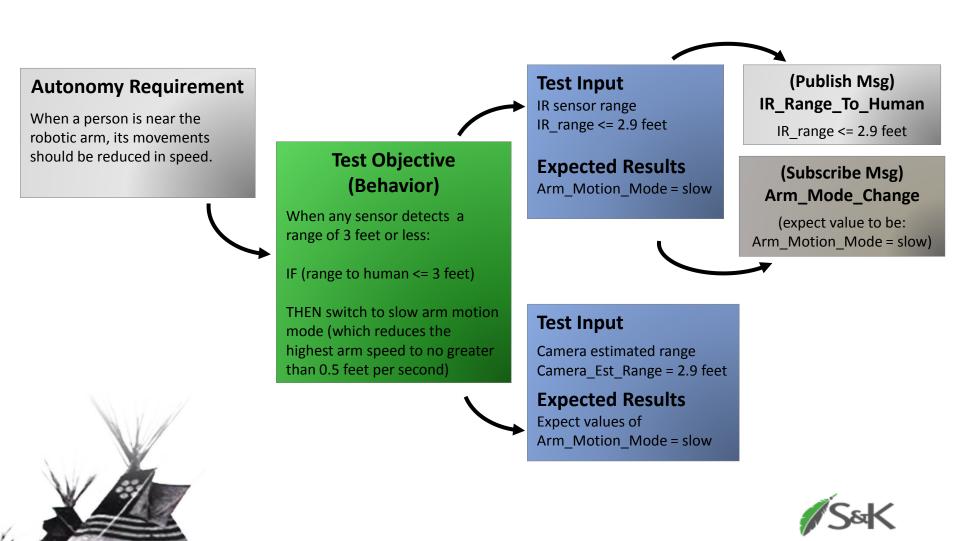
NASA Phase I SBIR sponsored by ARC & JSC

- Design eXtensible Markup Language (XML) schema to define cFS data models to support app-level testing
- Describe potential approaches for semi-autonomous test generation
- Design displays that support the management of requirements, test designs, and test results
- Develop a Concept of Operations (ConOps) with scenarios illustrating how ART supports people tasks
- Develop and demonstrate feasibility prototype

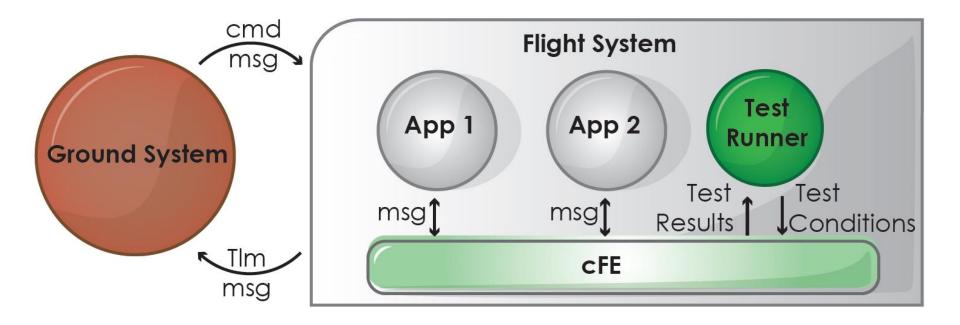




Represent Data for Progression from Requirement to Test Results



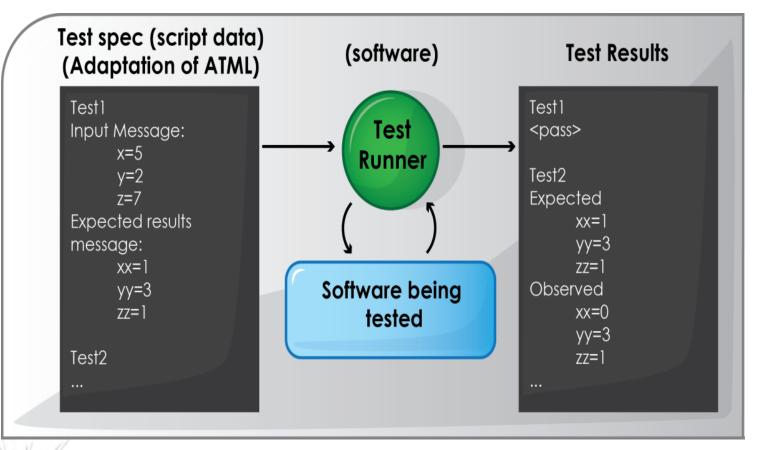
Test Runner Uses CFS Pub/Sub Architecture







Test Runner Reads Test Spec, Produces Test Results

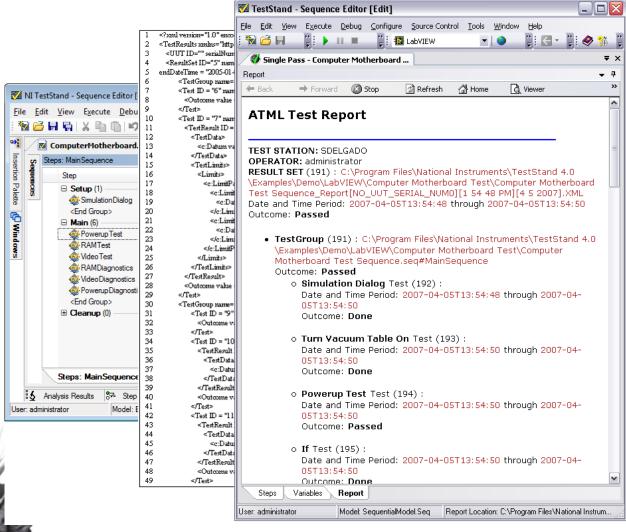


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Data Model Based on IEEE Standards

adopted by the Institute of Electrical and Electronics Engineers (IEEE) as a standard (IEEE Std 1671-2010)





Generating the Test Plan from Requirements

Observation from Phase I exercise with <u>APL Solar Probe</u> <u>Plus</u> type autonomy requirements

- Similarities from one requirement to the next
 - Often a tiered response, when first tier doesn't correct the issue, go to the next tier
 - Rule based behavior: If {condition} then {response}
- Similarities enable the formation of a <u>template</u> (illustrated on next page) for generating the test
- Some additional <u>parameters</u> are needed in addition to the template (illustrated on the page after that)



Generating Detailed Test Plans



- Detailed test plan can become tedious to specific in every detail
- Templates for a given set of requirements can remove some of the tedium
- Can encourage test-driven development





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Parameter Entry to Enable Test Generation from Template

Ent	Enter Design Values To Construct Initial Test									
AUT	AUT-3 Monitor Battery Temper				TemperatureAutonomy shall perform the following tiered response if the battery temperature is above a pre-defined limit: A) Soft-reset the PSC B) Power-cycle the PSC C) Switch the PSC (via a CIM side switch)					
Enter values from desig					design file					
Ν	M 5 N 6				Persistence (m of n)					
9	9				Max fire count					
2	2				Priority					
E	Enabled				Initial rule state (enabled/disabled)					
ł	bati	tery_	ten	np	Battery temperature variable name (default from rqts xml)					
-	160				pre-defined limit					
F	PSC_reset_cmd				Soft-reset the PSC command name (default from rqts xml)					
F	PSC_	pwr_	cycle	_cmd	Power-cycle the PSC command name (default from rqts xml)					
0	chan	ge_C	IM_s	ide_cm	witch CIM side command name (default from rqts xml)					



Template Contents for Generating Details of Test Specification

AUT-3	Monitor Battery Temperature	Autonomy shall perform the following tiered response if the
		battery temperature is above a pre-defined limit:
		A) Soft-reset the PSC
		B) Power-cycle the PSC
		C) Switch the PSC (via a CIM side switch)

- 1. Set nominal spacecraft system state
- 2. Verify autonomy takes no action
- 3. Inject fault
- 4. Verify faulted state (optional, especially level 0)
- 5. Verify autonomy response
- 6. Repeat steps 3-5 through all possible iterations
- 7. For tiered rule:
 - 1. Inject fault corrected by 1st action
 - 2. Inject fault corrected by 2d action
 - Inject fault corrected by 3d action
 - Inject unrecovered fault



8/1/2017 Slide 12



1st Part of Detailed Test Plan

Initialize

- 1. Set nominal spacecraft system state
 - set rule_fire_count(AUT3) to 0
 - set battery_temp = 159, every second
- 2. Verify autonomy takes no action
 - Wait 12 sec (m*2)
 - Verify rule_fire_count(AUT3)=0

Succeed on Tier 1 Response

- 1. Inject fault
 - set battery_temp = 161, every second
- 2. Verify tier 1 autonomy response
 - 1. Wait 7 sec (n+1)
 - 2. Verify PSC_reset cmd
 - Verify rule_fire_count(AUT3) = 1
- 3. Success
 - 1. set battery_temp = 159, every second
 - 2. Wait 15 sec (m * 3)
 - Expect rule_fire_count(AUT3) = 1
 - Verify that there is no:
 - 1. PSC_reset_cmd
 - PSC_pwr_reset_cmd
 - change_CIM_side_cmd

Succeed on Tier 2 Response

Succeed on Tier 3 Response

Unrecovered Function

Initialize

Succeed on Tier 1 Response

Succeed on Tier 2 Response

- 1. Inject fault
 - set battery_temp = 161, every second
- 2. Verify tier 2 autonomy response
 - 1. Wait 7 sec (n+1)
 - Verify PSC_pwr_reset_cmd
 - Verify rule_fire_count(AUT3) = 2
- 3. Success
 - 1. set battery_temp = 159, every second
 - 2. Wait 15 sec (m * 3)
 - Expect rule_fire_count(AUT3) = 2
 - Verify that there is no:
 - 1. PSC_reset_cmd
 - PSC_pwr_reset
 - change_CIM_side

Succeed on Tier 3 Response

Unrecovered Function



For Browsing Requirement

Autonomous Rule System Requirements Overview									
Requirements	Test Plans	Test Plans Test Results			Requirement AUT-3 Monitor Battery Temp				
Requirement ID	<u>T</u>	Title				AUT-3			
AUT-1	Detect Loss of Teleme	Detect Loss of Telemetry				Monitor Battery Temp			
AUT-2	Detect Invalid Telemet	Detect Invalid Telemetry				Device Health, Thermal Monitoring			
AUT-3	Monitor Battery Temp	Monitor Battery Temp				SC-1			
AUT-4	Monitor Battery State	Monitor Battery State of Charge				Protects against a potential PSC control fault that could cause excessive battery temperature.			
AUT-5	Detect Critically Low S	Detect Critically Low State of Charge			Details				
AUT-6	Battery Heater Power	Battery Heater Power On			battery	Autonomy shall perform the following tiered response if the battery temperature is above a pre-defined limit: A) Soft-Reset the PSC, B) Power-cycle the PSC, C) Switch the PSC (via a CIM side			
AUT-7	Battery Heater Power	Battery Heater Power Off			switch).				
				Condition	Battery_temp > 160				
					Auton	Autonomous system			
					Soft-reset the PSC, Power-cycle the PSC, Switch the PSC (via a CIM side switch)				
					PSC				
					160				
				Constraint	N/A				





Viewing Details from Test Plan

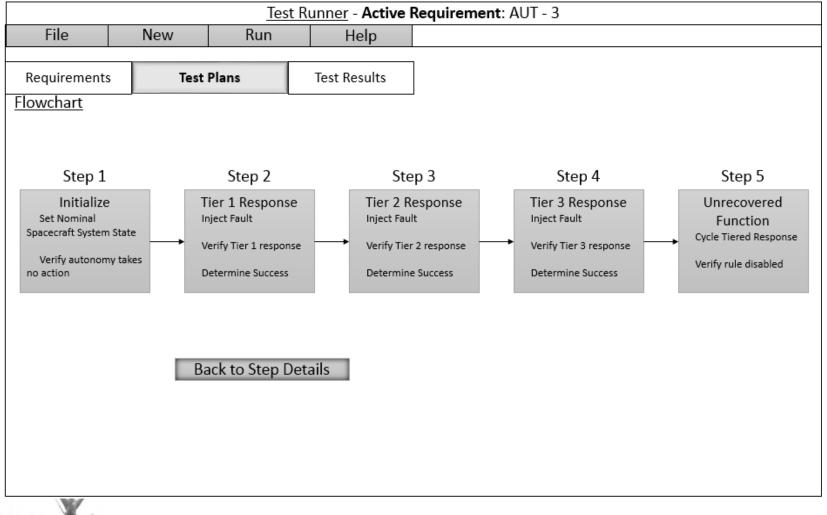
Test Runner - Active Requirement: AUT - 3								
File		New	Run	Help				
Requirements Test Plans Test Results			Step 2: Tier 1 Response					
Sequence					Injec	t fau	ılt 🔻	
Step ID Step Name						1.	Set battery_temp = 161, every 1 second	
Step 1	Initialize							
Step 2 Tier 1 Response				1				
Step 3 Tier 2 Response					Verif	-	r 1 autonomy response 🔍	
Step 4 Tier 3 Response				H I		1.	Wait 7 seconds	
Step 5 Unrecovered Function					2.	Verify <u>PSC_reset cmd</u>		
Step 5 Offictovered Punction						3.	Verify rule_fire_count(AUT3) = 1	
						,		
View	View Flowchart Add Test Plan Step					Succ	ess	
New Howchart Add lest Hall Step					1.	Set battery temp = 159, every 1 second		
					2.	Wait 15 seconds		
						3.	Expect rule_fire_count(AUT3) = 1	
							4.	Verify commands 🕨



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A Graphical View of the Test Plan





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A View of the Test Results

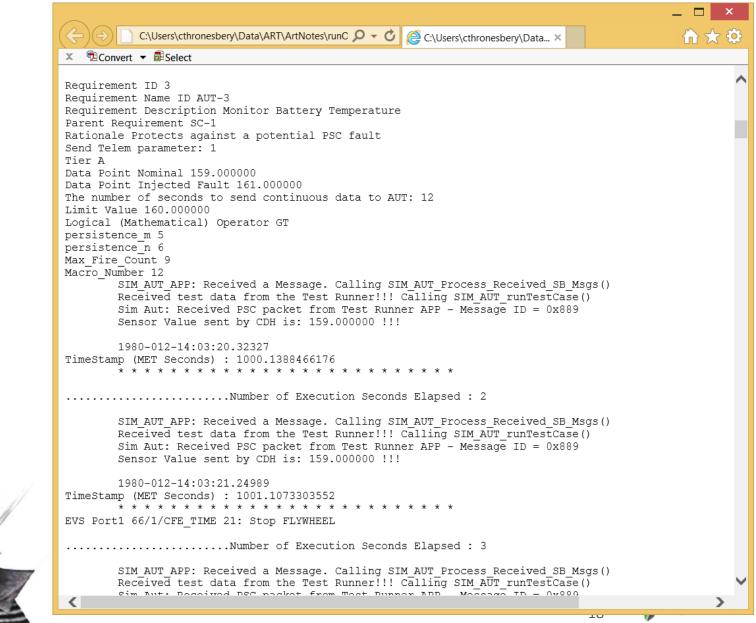
Test Runner - Active Requirement: AUT - 3									
File	File New Run		Help						
Requirement	s Tes	t Plans	Test Results	Initial autonomous rule test Results					
Date & Tim	e <u>Te</u> s	t Plan Name	Outcome	View Flowchart Export Test Results					
8/11/2016 - 9:3	AM Initial au	onomous rule te	est Failed 🗙	System: Autonomous Rule System Date: Aug. 11, 2016 – 9:34 AM Personnel Operator: Smith Johnson, sysop@company.com, 239-234-4321 Quality Assurance: Jane Smith, ga@company.com, 239-234-4321 Test Plan: Initial autonomous rule test Description: Performs macro command tests to verify that appropriate commands are being sent when the AUT rule is triggered. Outcome: Failed ✓ Step 1: Initialize ✓ Step 2: Tier 1 Response ✓ Step 3: Tier 2 Response ✓ Step 4: Tier 3 Response ✓ 1. Inject Fault X 2. Verify tier 3 autonomy response ✓ 1. Wait 7 seconds X 2. Verify change CIM side					



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TestRunner Detailed Results



ART Emphases

• Focus of Phase I (completed)

- Hard, real-time autonomy (low level of autonomy) and cFS
- Test-driven development
- Unit test for app in publish-subscribe architecture

Advantages of this approach

- Start test driven development early
- Ease the expression of autonomy requirements in terms of expected behavior
- Support pre-integration testing unit testing and regression testing
- Make integration testing time more productive no logic errors in software
- During integration, if software changes are required
 - If software changes are required
 - Make the changes
 - Re-run the pre-integration test to ensure no errors were inadvertently entered
 - Resuming integration testing





Innovations

- Represent <u>requirements and link with intended behaviors</u> for testing the requirements
- <u>Formal data models</u> for requirements, behavioral expectations, test specifications, and test results
- <u>Use of template</u> to drive the elaboration of test specifications
- Support for test driven development
- <u>Integration</u> of the <u>testing mechanism</u> with the <u>operational</u> environment to support (CFS users)
 - Enabled by modular architecture w/ pub-sub communications scheme
 - No change to the unit under test between testing and operations
 - Paves the way for runtime checkout routines for selected apps (e.g., sensors for deep-space science operations)
- Reporting of test <u>results</u> similar appearance to specifications, still linked to <u>requirements</u>





Next Steps

- Identification of how to support <u>higher levels of</u> <u>integration testing</u>:
- Identification of how to support additional types of <u>autonomy requirements</u>:
- Identification of additional options for semiautonomous <u>test generation</u>:
- Proof-of-principle prototype of ART
- Evaluation of proof-of-principle prototype



